

Notice of References CitedApplication/Control No.
09/229,592Applicant(s)/Patent Under
Reexamination
DOYLE ET AL.Examiner
Paul E Brock IIArt Unit
2815

Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,051,473 ✓	04-2000	Ishida et al.	438/300
	B	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wolf, Silicon Processing for the VLSI ERA, vol. 2: Process Integration, 1990
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.